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An interactive design automation system

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→ **ABSTRACT**

An interactive design automation system is presented which, after complete implementation, will allow the designer to check the determinacy and dead locks of the system before implementation. The design can be evaluated at various levels and modified interactively. The designer enters his design specification using either graphical representation or design language statements. The translator accepts the input and produces a data base for both the simulator and the logic synthesizer. The synthesizer accepts information from the function library of integrated circuits and designer-defined modules as well as the design constraints such as fan-in, fan-out and cost, and design goals set by the designer and produces a logic diagram. Simulators are utilized for design verification and performance evaluation at system and gate levels.

→ **REFERENCES**

Note: OCR errors may be found in this Reference List extracted from the full text article. ACM has opted to expose the complete List rather than only correct and linked references.

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